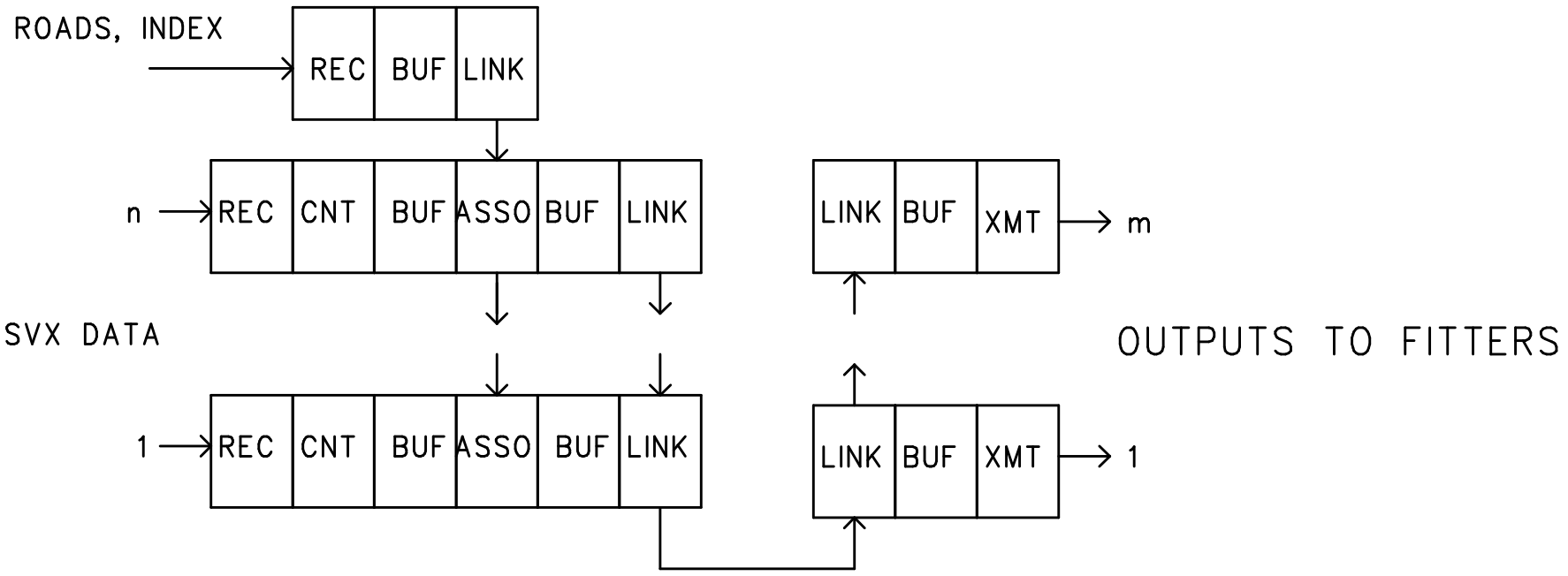
ASSOCIATOR MODULE – WITH CABLE OUTPUTS



A ROUTING INDEX HAS BEEN PRE–ASSIGNED TO EACH ROAD
ROAD COORDINATES ARE SENT TO ALL DSP'S
THE SVX INPUTS ARE COMPRESSED BY THE FPGA CENTROID PROCESSORS
THE BUFFERED CENTROID HITS ARE TRANSFERRED TO ASSOCIATORS
THE HITS ARE ASSOCIATED WITH ROADS IN THE FPGA/DSP PROCESSORS
OUTPUT DATA IS IS ROUTED TO M FITTER MODULES VIA m CABLES