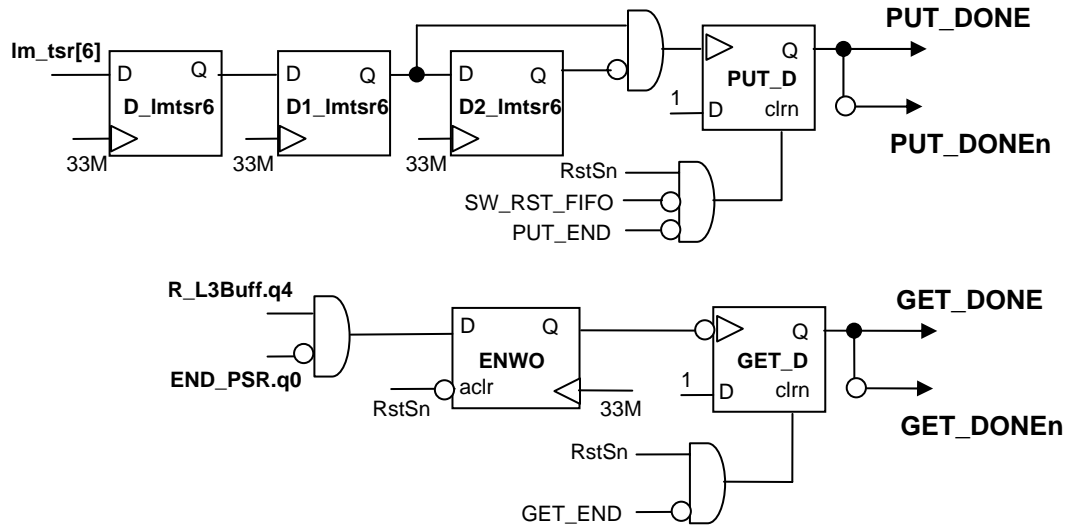


Buffer Controller: Signals to BM

code version: 20-May-03
 created in: *pci3_lm.tdf, b_ctrl.tdf*



Signal Transition Significance

- xxx_DONE = xxx_DONE[1] & xxx_DONE[2] & ... (active hi)
- xxx_DONEn = xxx_DONEn[1] # xxx_DONEn[2] # ... (active lo)
- xxx_DONE 0→1 all BC's done with PUT/GET
- xxx_DONEn 1→0 1st BC done with PUT/GET
- xxx_DONEn 0→1 all BC's received PUT/GET_END