

Test Points of FRC

T1: CS_BM#	T2: CS_SCLF#	T3: CS_TRDF#
T4: ENW_L3Data	T5: Mon_INT	T6: SCL_INT
T7: EN_MonO#	T8: I_DCLK	T9: I_nSTATUS
T10: Strobe_MSG (to BC)	T11: PUT_DONE	T12: PUT_DONE#
T13: GET_DONE	T14: GET_DONE#	T15: Mon_Start
T16: SCL_Ready	T17: EN_SCL_RST	T18: BM_TP1
T19: TRDF_L1Err	T20: R_BM_Mon	T21: BM_Strobe#
T22: TRDF_Strobe#	T23: SCL_S0#	T24: SCL_S1#
T25: SCL_S2#	T26: PCI1_TP1	T27: R_SCLF_Mon
T28: Test/DAQ	T29: ENW_PCI12FIFO	T30: EN_Testdelay (EN_TestData)
T31: LinkRDY#	T32: DAV#	T33: CAV#
T34: CPU_Clr_Mon	T35: R/W	T36: EN_Transfer
T37: SLV_DONE#	T38: SLV_RDY#	T39: DB_L3_Data
T40: I_CONF_DONE	T41: PCI3_TP3	T42: PCI3_TP2
T43: PCI3_TP1	T44: TRDF_TP1	T45: TRDF_TP2
T46: PCI1_TP2	T47: PCI1_TP3	T48: PCI2_TP1
T49: PCI2_TP2	T50: PCI2_TP3	T51: SCLF_TP1
T52: SCLF_TP2	T53: BM_TP2	