

## Test Points of FRC (prototype)

T1: CS_BM#	T2: CS_SCLF#	T3: CS_TRDF#
T4: ENW_L3Data	T5: Mon_INT	T6: SCL_INT
T7: EN_MonO#	T8: I_DCLK	T9: I_nSTATUS
T10: Strobe_MSG (to BC)	T11: PUT_DONE	T12: PUT_DONE#
T13: GET_DONE	T14: GET_DONE#	T15: Mon_Start
T16: SCL_Ready	T17: EN_SCL_RST	T18: Start_BM
T19: TRDF_L1Err	T20: R_BM_Mon	T21: BM_Strobe#
T22: TRDF_Strobe#	T23: SCL_S0#	T24: SCL_S1#
T25: SCL_S2#	T26: PCI1_TP1	T27: R_SCLF_Mon
T28: Test/DAQ	T29: ENW_PCI12FIFO	T30: EN_Testdelay (EN_TestData)
T31: LinkRDY#	T32: DAV#	T33: CAV#
T34: CPU_Clr_Mon	T35: R/W	T36: EN_Transfer
T37: SLV_DONE#	T38: SLV_RDY#	T39: DB_L3_Data
T40: I_CONF_DONE	T41: PCI3_TP3	T42: PCI3_TP2
T43: PCI3_TP1	T44: Normal_EOE	T45: CTT_Avail
T46: lm_ACK#_PCI1	T47: lm_DXFR#_PCI1	T48: lm_RDY#_PCI2
T49: lm_ACK#_PCI2	T50: lm_DXFR#_PCI2	