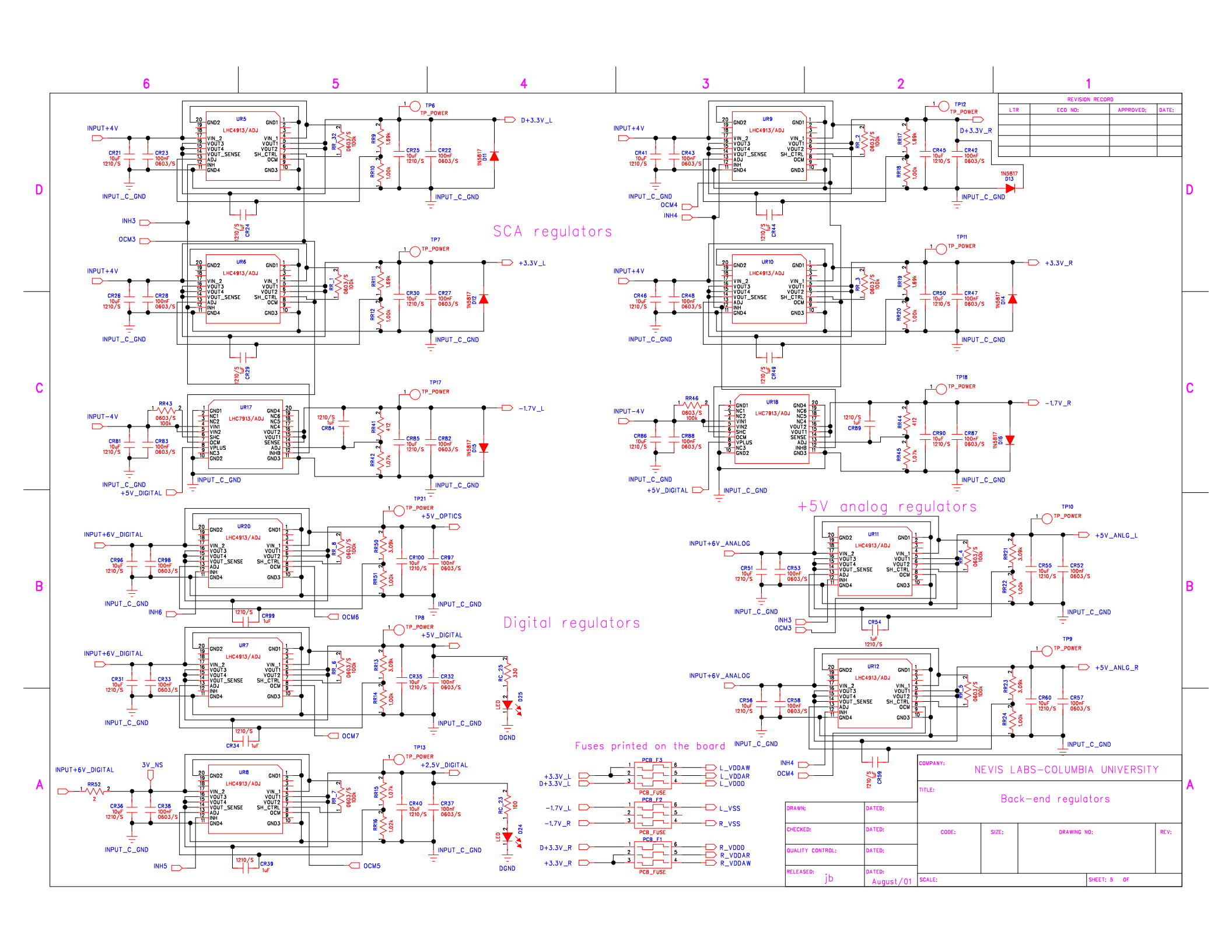
SCA regulators
+5V analog regulators
Digital regulators
Fuses printed on the board
REVISION RECORD
LTR
ECO NO:
APPROVED:
DATE:
INPUT+4V
INPUT-4V
INPUT+6V_DIGITAL
INPUT+6V_ANALOG
INPUT_C_GND
+5V_DIGITAL
D+3.3V_L
+3.3V_L
-1.7V_L
+5V_OPTICS
+5V_ANLG_L
D+3.3V_R
+3.3V_R
-1.7V_R
+5V_ANLG_R
+2.5V_DIGITAL
3V_NS
INH3
INH4
INH5
INH6
OCM3
OCM4
OCM5
OCM6
OCM7
DGND
L_VDDAW
L_VDDAR
L_VDDD
L_VSS
R_VSS
R_VDDD
R_VDDAR
R_VDDAW
PCB_FUSE
COMPANY:
NEVIS LABS-COLUMBIA UNIVERSITY
TITLE:
Back-end regulators
DRAWN:
DATED:
CHECKED:
QUALITY CONTROL:
RELEASED:
jb
August/01
CODE:
SIZE:
DRAWING NO:
REV:
SCALE:
SHEET: 5 OF