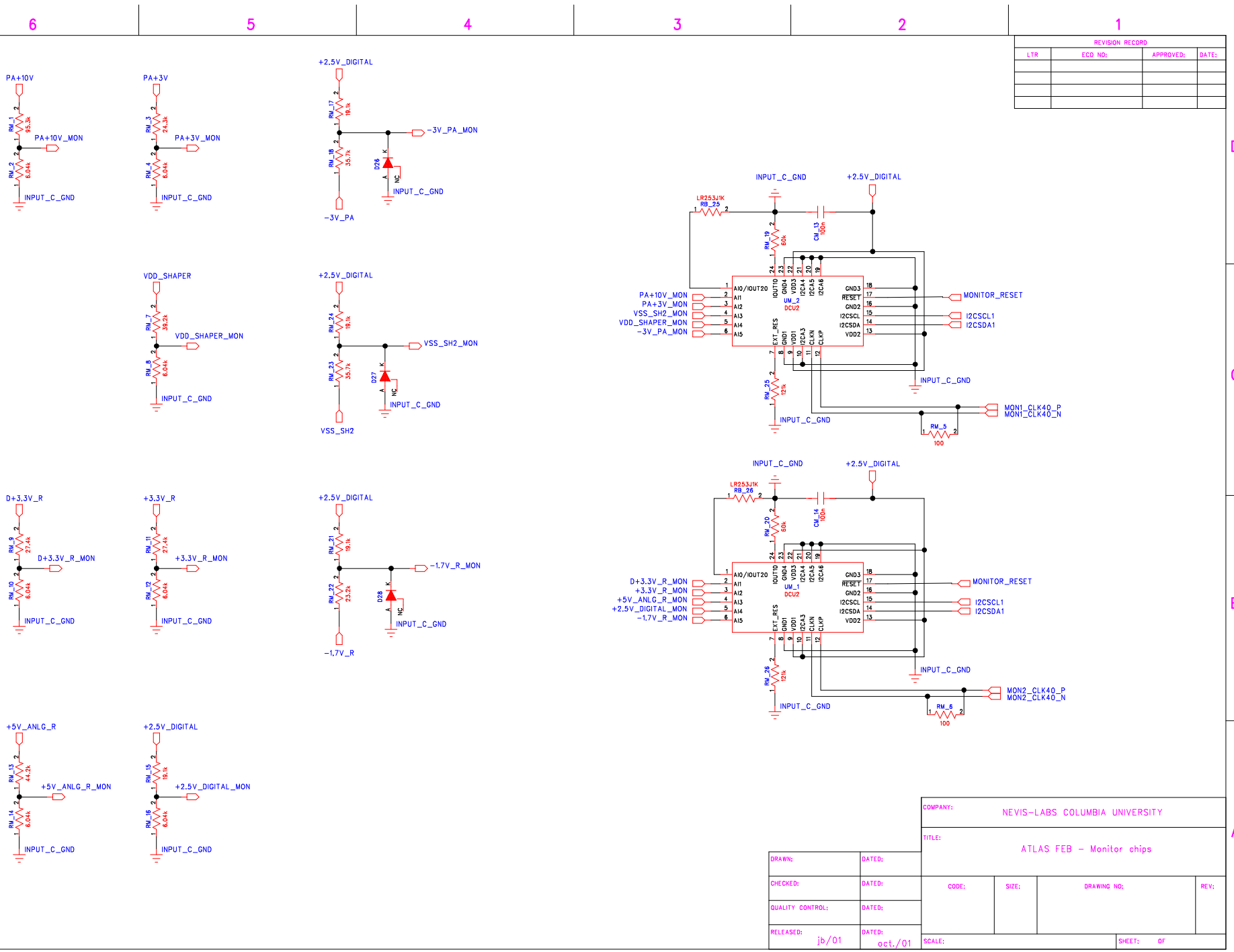
6
5
4
3
2
1
D
C
B
A
REVISION RECORD
LTR
ECO NO:
APPROVED:
DATE:
PA+10V
RM_1
95.3k
PA+10V_MON
RM_2
6.04k
INPUT_C_GND
PA+3V
RM_3
24.3k
PA+3V_MON
RM_4
6.04k
INPUT_C_GND
+2.5V_DIGITAL
RM_17
19.1k
RM_18
35.7k
−3V_PA_MON
D26
K
A
NC
INPUT_C_GND
−3V_PA
VDD_SHAPER
RM_7
39.2k
VDD_SHAPER_MON
RM_8
6.04k
INPUT_C_GND
+2.5V_DIGITAL
RM_24
19.1k
RM_23
35.7k
VSS_SH2_MON
D27
K
A
NC
INPUT_C_GND
VSS_SH2
D+3.3V_R
RM_9
27.4k
D+3.3V_R_MON
RM_10
6.04k
INPUT_C_GND
+3.3V_R
RM_11
27.4k
+3.3V_R_MON
RM_12
6.04k
INPUT_C_GND
+2.5V_DIGITAL
RM_21
19.1k
RM_22
23.2k
−1.7V_R_MON
D28
K
A
NC
INPUT_C_GND
−1.7V_R
+5V_ANLG_R
RM_13
44.2k
+5V_ANLG_R_MON
RM_14
6.04k
INPUT_C_GND
+2.5V_DIGITAL
RM_15
19.1k
+2.5V_DIGITAL_MON
RM_16
6.04k
INPUT_C_GND
INPUT_C_GND
+2.5V_DIGITAL
LR253J1K
RB_25
RM_19
60k
CM_13
100n
UM_2
DCU2
AIO/IOUT20
AI1
AI2
AI3
AI4
AI5
IOUT10
GND4
VDD3
I2CA4
I2CA5
I2CA6
EXT_RES
GND1
VDD1
I2CA3
CLKN
CLKP
GND3
RESET
GND2
I2CSCL
I2CSDA
VDD2
PA+10V_MON
PA+3V_MON
VSS_SH2_MON
VDD_SHAPER_MON
−3V_PA_MON
MONITOR_RESET
I2CSCL1
I2CSDA1
RM_25
121k
INPUT_C_GND
MON1_CLK40_P
MON1_CLK40_N
RM_5
100
INPUT_C_GND
+2.5V_DIGITAL
LR253J1K
RB_26
RM_20
60k
CM_14
100n
UM_1
DCU2
D+3.3V_R_MON
+3.3V_R_MON
+5V_ANLG_R_MON
+2.5V_DIGITAL_MON
−1.7V_R_MON
MONITOR_RESET
I2CSCL1
I2CSDA1
RM_26
121k
INPUT_C_GND
MON2_CLK40_P
MON2_CLK40_N
RM_6
100
COMPANY:
NEVIS-LABS COLUMBIA UNIVERSITY
TITLE:
ATLAS FEB – Monitor chips
DRAWN:
DATED:
CHECKED:
DATED:
QUALITY CONTROL:
DATED:
RELEASED:
jb/01
DATED:
oct./01
CODE:
SIZE:
DRAWING NO:
REV:
SCALE:
SHEET:
OF