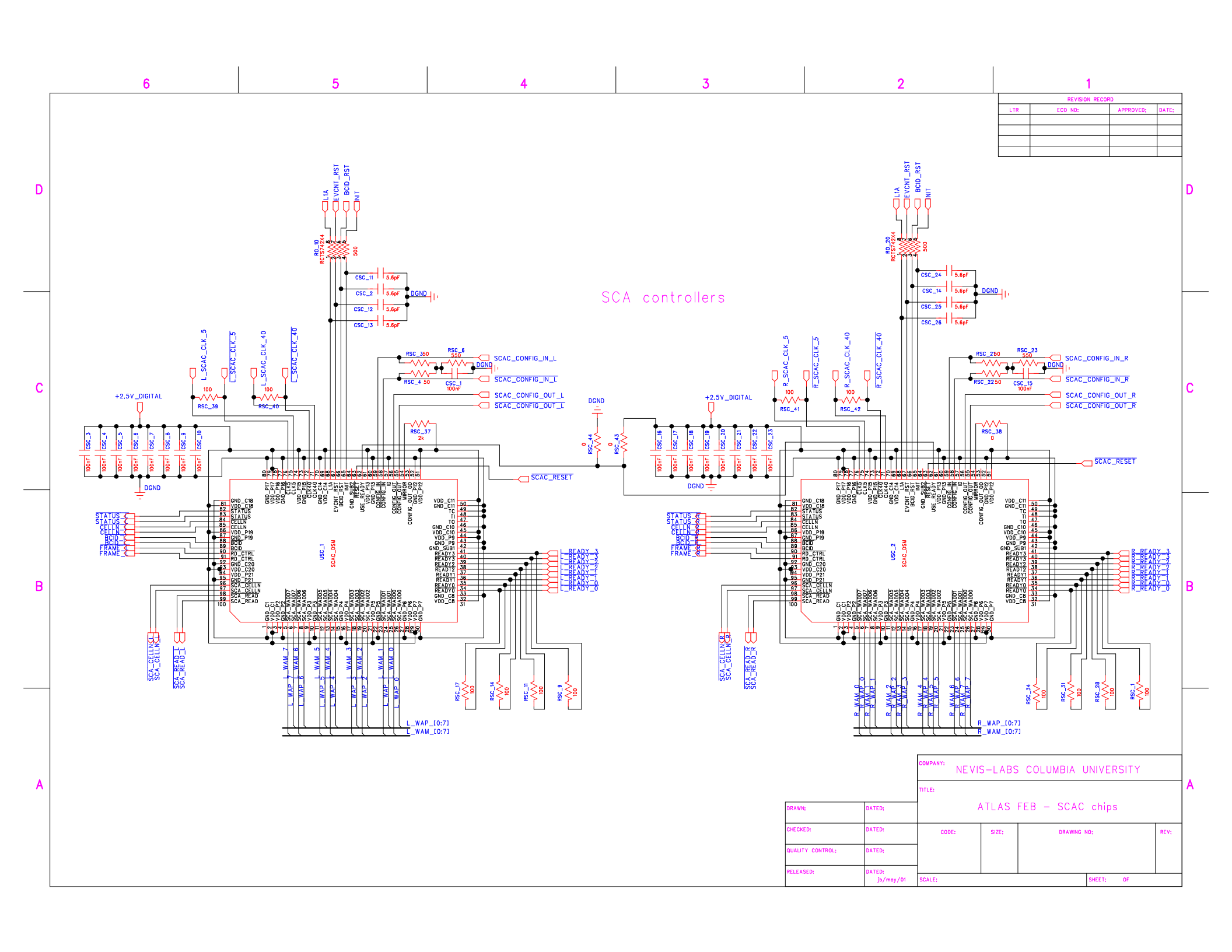
SCA controllers
REVISION RECORD
LTR
ECO NO:
APPROVED:
DATE:
L1A
EVCNT_RST
BCID_RST
INT
+2.5V_DIGITAL
DGND
SCAC_RESET
SCAC_CONFIG_IN_L
SCAC_CONFIG_OUT_L
SCAC_CONFIG_IN_R
SCAC_CONFIG_OUT_R
L_SCAC_CLK_5
L_SCAC_CLK_40
R_SCAC_CLK_5
R_SCAC_CLK_40
USC_1
USC_2
SCAC_DSM
L_WAP_[0:7]
L_WAM_[0:7]
R_WAP_[0:7]
R_WAM_[0:7]
COMPANY:
NEVIS-LABS COLUMBIA UNIVERSITY
TITLE:
ATLAS FEB - SCAC chips
DRAWN:
CHECKED:
QUALITY CONTROL:
RELEASED:
DATED:
jb/may/01
CODE:
SIZE:
DRAWING NO:
REV:
SCALE:
SHEET:
OF