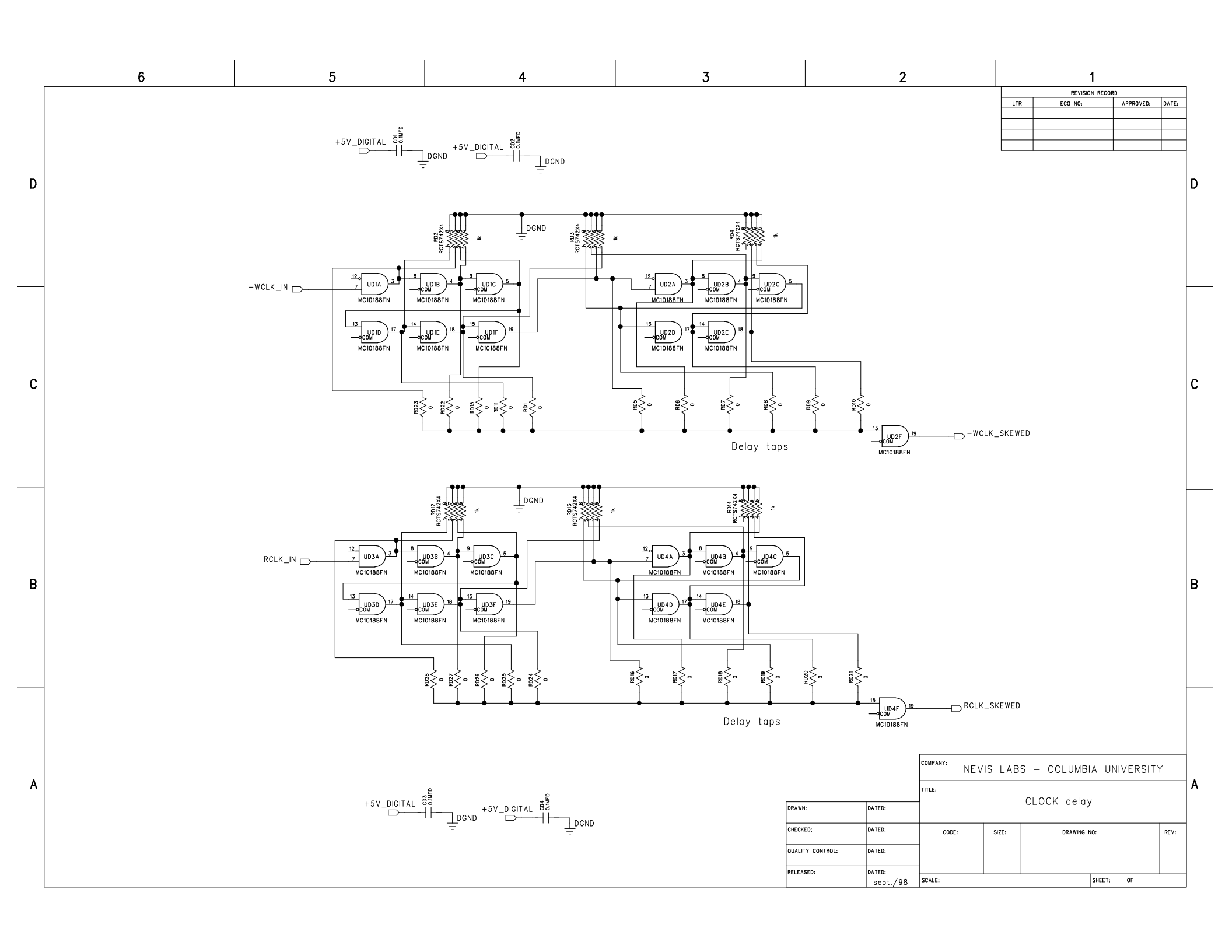
6
5
4
3
2
1
REVISION RECORD
LTR
ECO NO:
APPROVED:
DATE:
+5V_DIGITAL
CD1 0.1MFD
DGND
+5V_DIGITAL
CD2 0.1MFD
DGND
DGND
RD2 RC1S742X4 1k
RD3 RC1S742X4 1k
RD4 RC1S742X4 1k
-WCLK_IN
UD1A
UD1B
UD1C
UD1D
UD1E
UD1F
UD2A
UD2B
UD2C
UD2D
UD2E
UD2F
MC10188FN
RD23 0
RD22 0
RD15 0
RD11 0
RD1 0
RD5 0
RD6 0
RD7 0
RD8 0
RD9 0
RD10 0
-WCLK_SKEWED
Delay taps
DGND
RD12 RC1S742X4 1k
RD13 RC1S742X4 1k
RD14 RC1S742X4 1k
RCLK_IN
UD3A
UD3B
UD3C
UD3D
UD3E
UD3F
UD4A
UD4B
UD4C
UD4D
UD4E
UD4F
MC10188FN
RD28 0
RD27 0
RD26 0
RD25 0
RD24 0
RD16 0
RD17 0
RD18 0
RD19 0
RD20 0
RD21 0
RCLK_SKEWED
Delay taps
+5V_DIGITAL
CD3 0.1MFD
DGND
+5V_DIGITAL
CD4 0.1MFD
DGND
COMPANY:
NEVIS LABS – COLUMBIA UNIVERSITY
TITLE:
CLOCK delay
DRAWN:
DATED:
CHECKED:
DATED:
QUALITY CONTROL:
DATED:
RELEASED:
DATED:
sept./98
CODE:
SIZE:
DRAWING NO:
REV:
SCALE:
SHEET: OF