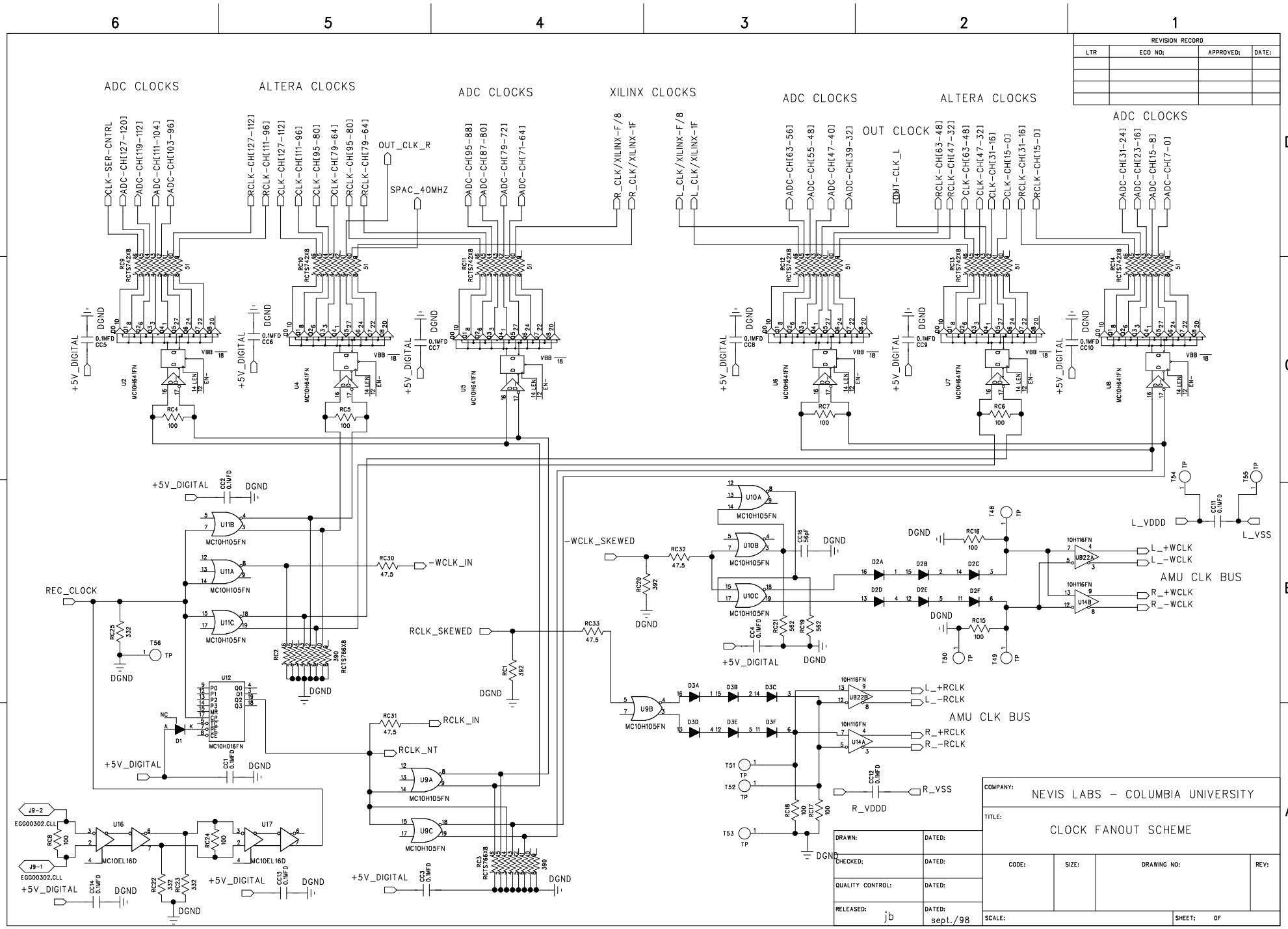

REVISION RECORD
LTR
ECO NO:
APPROVED:
DATE:
ADC CLOCKS
ALTERA CLOCKS
ADC CLOCKS
XILINX CLOCKS
ADC CLOCKS
ALTERA CLOCKS
ADC CLOCKS
OUT CLOCK
OUT_CLK_R
SPAC_40MHZ
OUT_CLK_L
CLK-SER-CNTRL
ADC-CH[127-120]
ADC-CH[119-112]
ADC-CH[111-104]
ADC-CH[103-96]
RCLK-CH[127-112]
RCLK-CH[111-96]
CLK-CH[127-112]
CLK-CH[111-96]
CLK-CH[95-80]
CLK-CH[79-64]
RCLK-CH[95-80]
RCLK-CH[79-64]
ADC-CH[95-88]
ADC-CH[87-80]
ADC-CH[79-72]
ADC-CH[71-64]
R_CLK/XILINX-F/8
R_CLK/XILINX-1F
L_CLK/XILINX-F/8
L_CLK/XILINX-1F
ADC-CH[63-56]
ADC-CH[55-48]
ADC-CH[47-40]
ADC-CH[39-32]
RCLK-CH[63-48]
RCLK-CH[47-32]
CLK-CH[63-48]
CLK-CH[47-32]
CLK-CH[31-16]
CLK-CH[15-0]
RCLK-CH[31-16]
RCLK-CH[15-0]
ADC-CH[31-24]
ADC-CH[23-16]
ADC-CH[15-8]
ADC-CH[7-0]
+5V_DIGITAL
DGND
VBB
MC10H641FN
MC10H105FN
MC10H016FN
MC10EL16D
MC10H116FN
-WCLK_IN
-WCLK_SKEWED
REC_CLOCK
RCLK_SKEWED
RCLK_IN
RCLK_NT
L_VDDD
L_VSS
L_+WCLK
L_-WCLK
R_+WCLK
R_-WCLK
AMU CLK BUS
L_+RCLK
L_-RCLK
R_+RCLK
R_-RCLK
R_VDDD
R_VSS
EGG00302.CLL
COMPANY:
NEVIS LABS - COLUMBIA UNIVERSITY
TITLE:
CLOCK FANOUT SCHEME
DRAWN:
DATED:
CHECKED:
DATED:
QUALITY CONTROL:
DATED:
RELEASED:
jb
DATED:
sept./98
CODE:
SIZE:
DRAWING NO:
REV:
SCALE:
SHEET:
OF