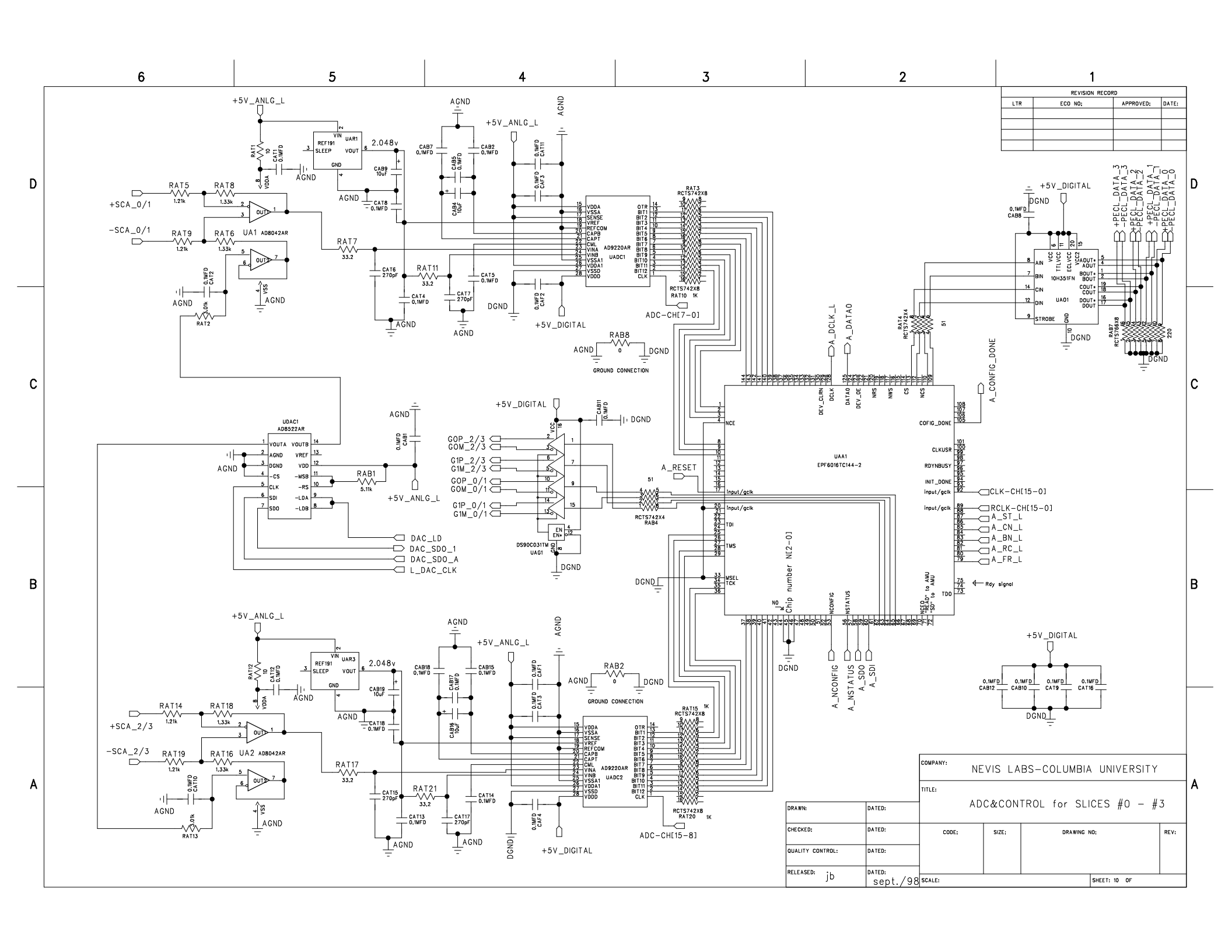
REVISION RECORD
LTR
ECO NO:
APPROVED:
DATE:
+5V_ANLG_L
AGND
DGND
+5V_DIGITAL
2.048v
UA1 AD8042AR
UA2 AD8042AR
AD9220AR
UADC1
UADC2
RCTS742X8
RCTS742X4
RCTS786X8
ADC-CH[7-0]
ADC-CH[15-8]
GROUND CONNECTION
UDAC1
AD8522AR
DAC_LD
DAC_SDO_1
DAC_SDO_A
L_DAC_CLK
A_DCLK_L
A_DATA0
A_RESET
A_CONFIG_DONE
UAA1
EPF6016TC144-2
Chip number NC2-01
CLK-CH[15-0]
RCLK-CH[15-0]
A_ST_L
A_CN_L
A_BN_L
A_RC_L
A_FR_L
Rdy signal
A_NCONFIG
A_NSTATUS
A_SDO
A_SDI
UAO1
10H351FN
+PECL_DATA_3
-PECL_DATA_3
+PECL_DATA_2
-PECL_DATA_2
+PECL_DATA_1
-PECL_DATA_1
+PECL_DATA_0
-PECL_DATA_0
+SCA_0/1
-SCA_0/1
+SCA_2/3
-SCA_2/3
COMPANY:
NEVIS LABS-COLUMBIA UNIVERSITY
TITLE:
ADC&CONTROL for SLICES #0 - #3
DRAWN:
DATED:
CHECKED:
QUALITY CONTROL:
RELEASED:
jb
sept./98
CODE:
SIZE:
DRAWING NO:
REV:
SCALE:
SHEET: 10 OF