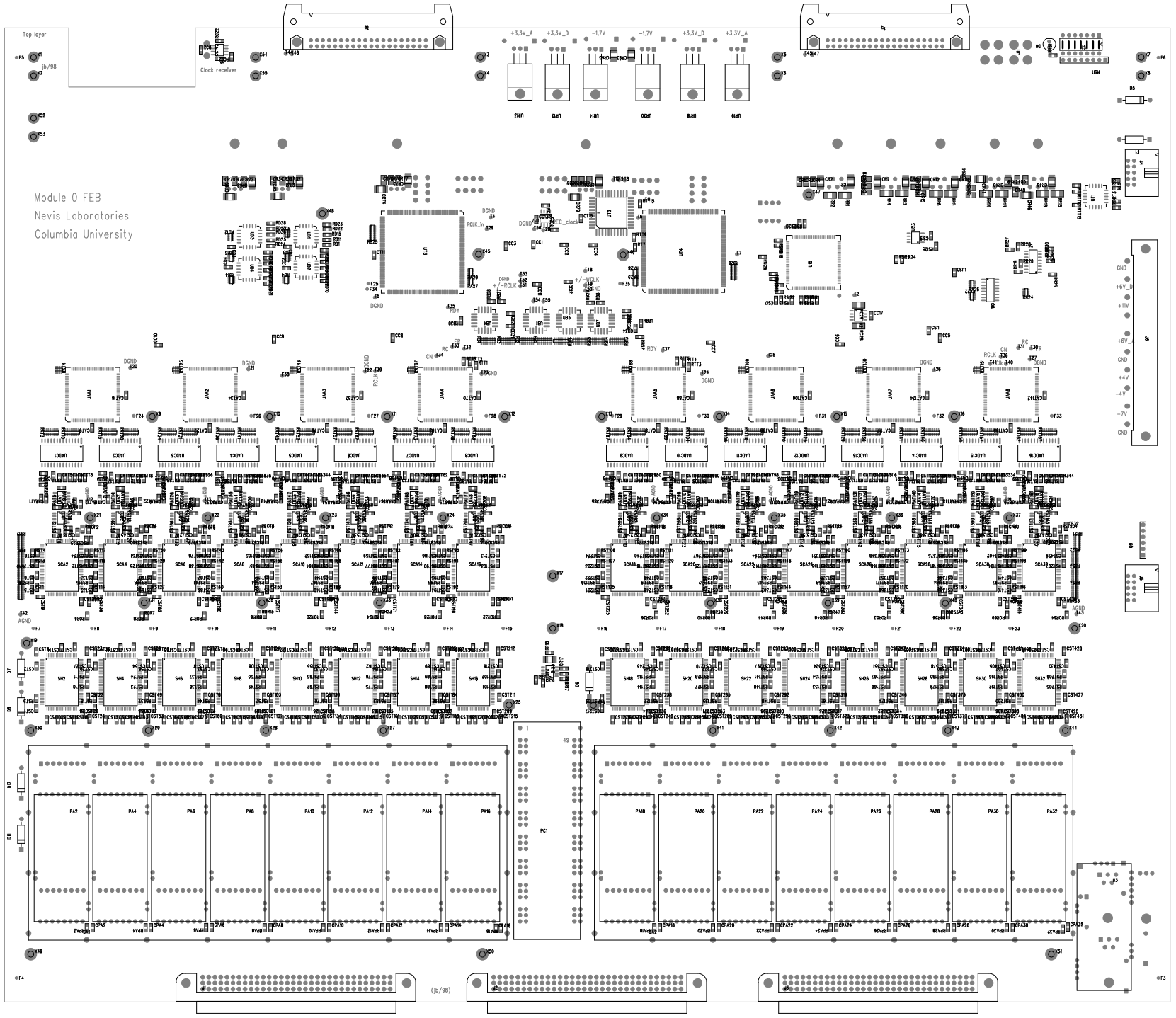
Top layer
Module 0 FEB
Nevis Laboratories
Columbia University
Clock receiver
+3,3V_A
+3,3V_D
-1,7V
-1,7V
+3,3V_D
+3,3V_A
DGND
AGND
RCLK_in
+/-RCLK
+/-WCLK
RDY
CN
RC
RCLK
+6V_D
+11V
+6V_A
+4V
-4V
-7V
PC1
(Jb/98)