

1	2	3	4	5	6	7	8	9	1
N	Pin	C	Dire	FEM Signal	Set	FEM	Pro	Description	N
1	SCL	14	I/O	BSCL	lwire	1		I2C clock. Goes to 1Wire master	1
2	SDA	14	I/O	BSDA	lwire	3		I2C data	2
3	LTPU	14	X		n/a			ETPU	3
4	CLKOUT	11	O		CLK			Main CPU clock	4
5	RESET*	14	I		RESET			To simulate RESET button	5
6	RSTO*	14	O		RESET			Generated when CPU is reset i	6
7	TEA*	11	I		DMA			Indicates an error condition exist	7
8	TIP*	11	O		DMA			Bus control output signal indica	8
9	TSIZ0*	P8	O		DMA			Data bus width 8,16 or 32	9
10	TS*	11	O		DMA			Bus control output signal indica	10
11	TSIZ1*	N8	O		DMA			Data bus width 8,16 or 32	11
12	CS7*	11	O		DMA				12
13	CS6*	11	O		DMA				13
14	CS5*	11	O		DMA				14
15	CS4*	14	O		DMA				15
16	CS3*	11	O		DMA				16
17	CS2*	11	O		DMA				17
18	CS1*	11	O		DMA				18
19	DT3IN	15	I	FTDO	JTAG	5	6	4 JTAG TDO	19
20	DT3OUT	16	O	CS3	QSPI	29	30	5 Board selection	20
21	DT2IN	10	O	FTMS	JTAG	23	24	0 JTAG TMS	21
22	DT2OUT	11	O	FTDI	JTAG	25	26	1 JTAG TDI	22
23	DT1IN	P7	O	CS2	QSPI	19	20	2 Board selection	23
24	DT1OUT	R7	O	FTCK	JTAG	21	22	3 JTAG TCK	24
25	DT0IN	G4	O		USER	15	16	Reserved	25
26	DT0OUT	G3	O		USER	17	18	Reserved	26
27	U2TXD	11	O		USER			Could be configured to GPIO in	27
28	U2RXD	11	I		USER			Could be configured to GPIO in	28
29	U1CTS	11	I		USER			Could be configured as GPIO in	29
30	U1RTS	11	O		USER			Could be configured as GPIO in	30
31	UTXD0	H2	O		n/a			Connected to UART port	31
32	URXD0	G2	I		n/a				32
33	CAN0RX	A1	I		n/a				33
34	CAN0TX	A1	O		n/a				34
35	QSPI_CS1	11	O	CS1	QSPI	11	12	Board selection	35
36	QSPI_CS0	D9	O	CS0	QSPI	13	14	Board selection	36
37	QSPI_CLK	B8	O	QSPI_CLK	QSPI	9	10	6	37
38	QSPI_DIN	C8	I	QSPI_DIN	QSPI	27	28	7	38
39	QSPI_DOUT	D8	O	QSPI_DOUT	QSPI	7	8	8	39
40	IRQ7*	R8	I	IRQ7*	IRQ	31	32	Abort button	40
41	IRQ6*	T8	I		IRQ				41
42	IRQ5*	N9	I		IRQ				42
43	IRQ4*	P9	I		IRQ				43
44	IRQ3*	R9	I		IRQ				44
45	IRQ2*	T9	I		IRQ				45
46	IRQ1*	11	I		IRQ				46
47	GND					2,4	33,34		47
48	+3.3V								48
49	CAN_HI								49
50	CAN_LO								50
				LED1				13	
				LED2				12	
				OTXEN1				14	
				OSD1				15	