

MCU_Port MCF5282

1	2	3	4	5	6	7	8	9
N	Pin	CPU	Direct	Signal	Set		Probe	Description
1	SCL		I/O		1wire			I2C clock. Goes to 1Wire master chip to control 1Wire devices
2	SDA		I/O		1wire			I2C data
3	CANTX							
4	CANRX							
5	VRH							
6	VDH							
7	AN56							
8	AN55							
9	AN53							
10	AN52							
11	AN3							
12	AN2							
13	AN1							
14	AN0							
15	DTIN3		O	TST[3]	Debug			
16	DTOUT3		O	TST[2]	Debug			
17	DTIN2		O	TST[1]	Debug			
18	DTOUT2		O	TST[0]	Debug			
19	DTIN1		O	JTAG_TMS	JTAG			JTAG TMS
20	DTOUT1		O	JTAG_TDI	JTAG			JTAG TDI
21	DTIN0		I	JTAG_TDO	JTAG			JTAG TDO
22	DTOUT0		O	JTAG_TCK	JTAG			JTAG TCK
23	GPTB3							
24	GPTB2							
25	GPTB1							
26	GPTB0							
27	GPTA3							
28	GPTA2							
29	GPTA1							
30	GPTA0							
31	UTXD1							
32	URXD1							
33	UTXD0							
34	URXD0							
35	QSPI_CS3				QSPI			
36	QSPI_CS2				QSPI			
37	QSPI_CS1				QSPI			
38	QSPI_CS0				QSPI			
39	QSPI_CLK				QSPI			
40	QSPI_DIN				QSPI			
41	QSPI_DOUT				QSPI			
42	IRQ7*							
43	IRQ6*							
44	IRQ5*							
45	IRQ4*							
46	IRQ3*							
47	IRQ2*							
48	IRQ1*							
49	+3.3V							
50	GND				GND			