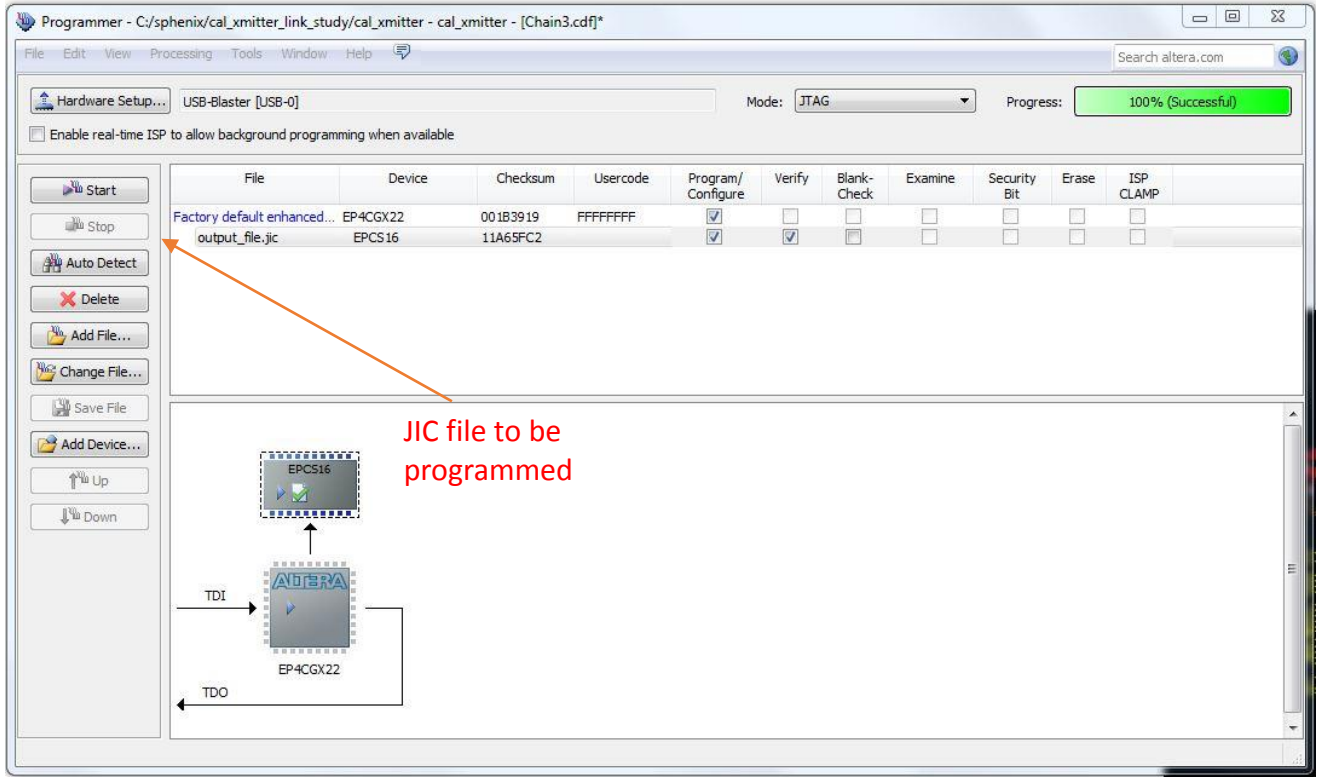
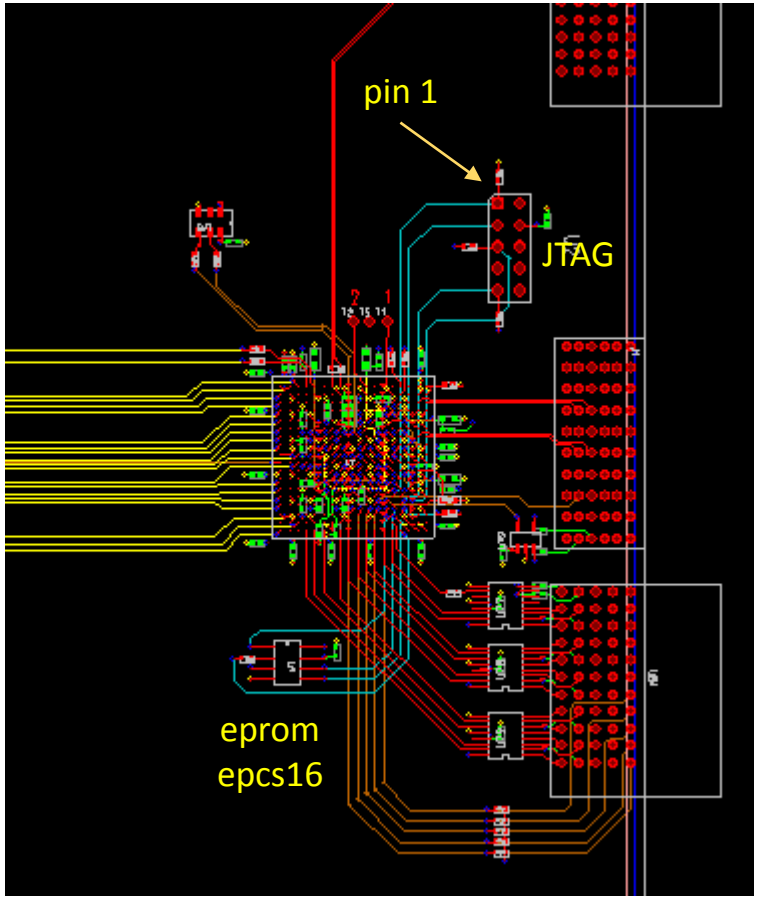
SPHENIX XMIT EPROM Programming.

The XMIT program is through JTAG, 1st it will program the FPGA via factory program
(so the FPGA will act as bridge between JTAG and EPROM) and 2nd it will program the EPROM through JTAG & FPGA

1) Power off the board
2) Connect the USB programmer
3) Power on the board
4) Bring up the Altera programmer software, add jic file to the programmer. Check to make sure the devices is correct in the programmer
5) Program the EPROM
6) Power off the device and Disconnect the USB programmer
7) power on the device.

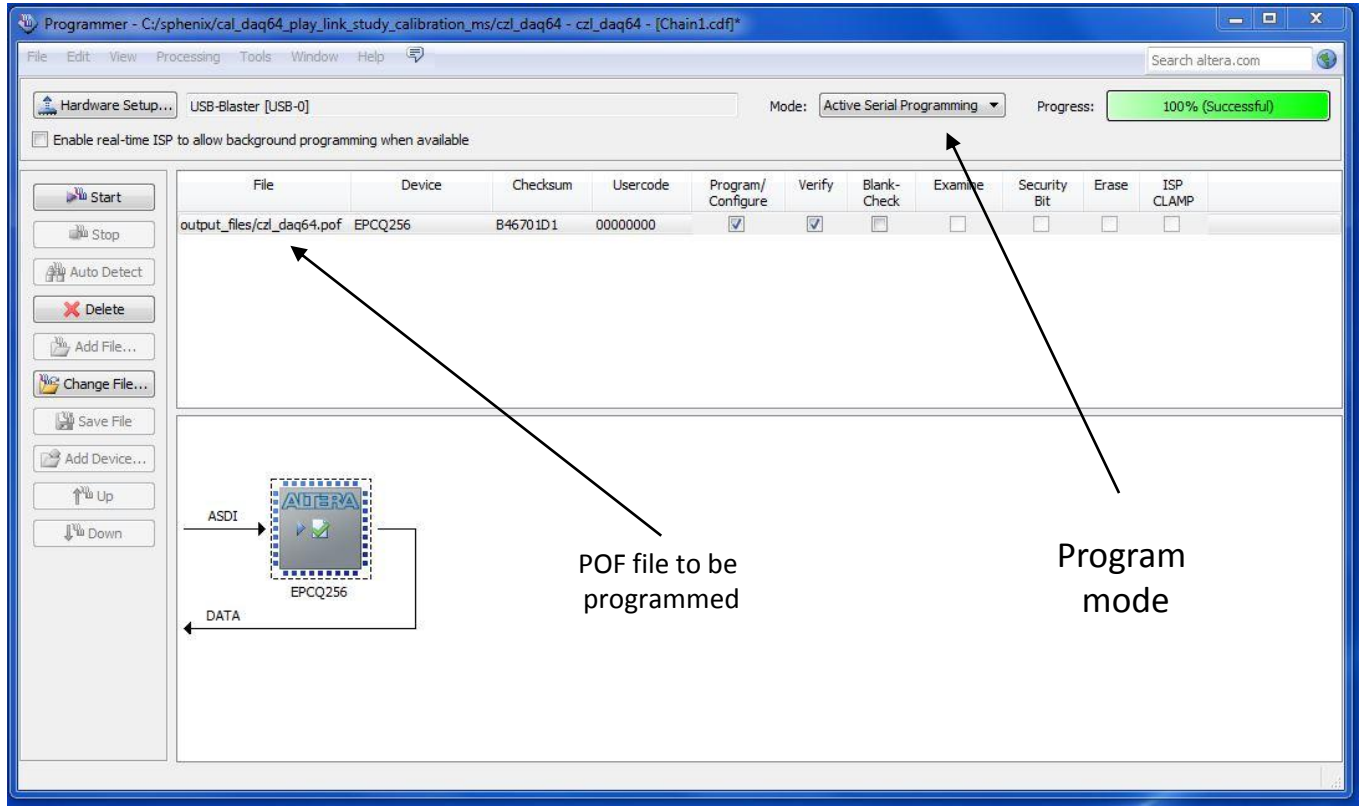


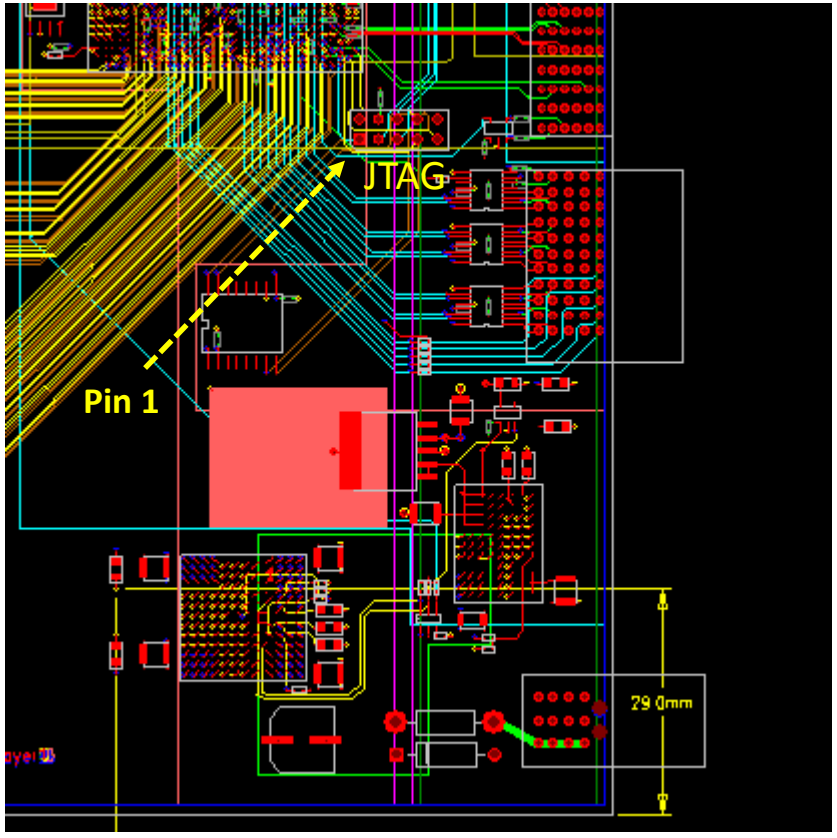


SPHENIX ADC EPROM Programming.

The ADC board Arria 5 FPGA is located in rear middle of the ADC board.

1) Power off the board
2) Connect the USB programmer
3) Power on the board
4) Bring up the Altera programmer software, add pof file to the programmer. Check to make sure the devices is correct in the programmer
5) Program the EPROM
6) Power off the device and Disconnect the USB programmer
7) power on the device.





SPHENIX Crate Controller EPROM Programming.

The Cyclone FPGA is located in rear middle of the controller board.

1) Power off the board
2) Connect the USB programmer
3) Power on the board
4) Bring up the Altera programmer software, add pof file to the programmer. Check to make sure the devices is correct in the programmer
5) Program the EPROM
6) Power off the device and Disconnect the USB programmer
7) power on the device.