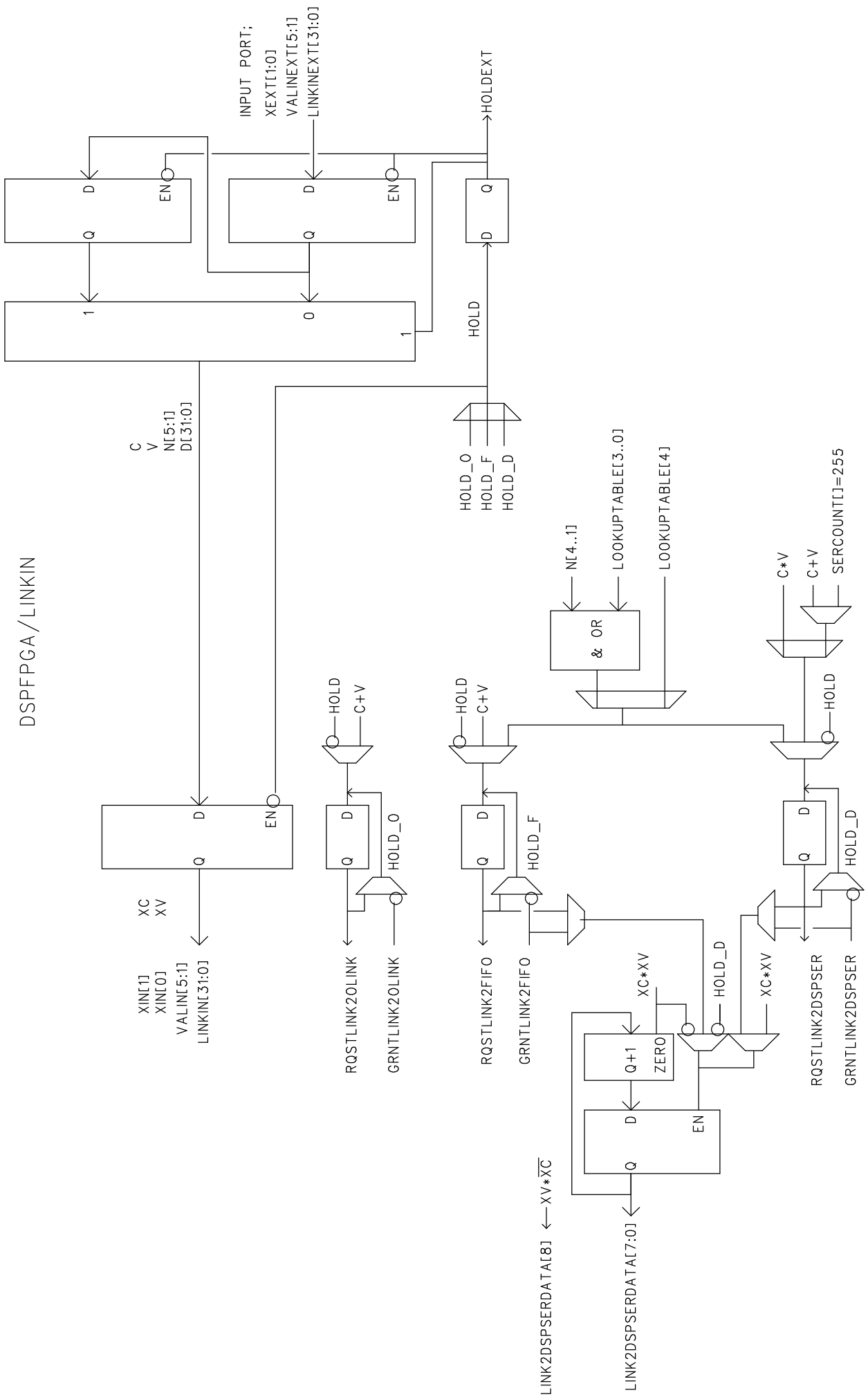
DSPFPGA/LINKIN
INPUT PORT;
XEXT[1:0]
VALINEXT[5:1]
LINKINEXT[31:0]
HOLDEXT
HOLD
HOLD_O
HOLD_F
HOLD_D
C
V
N[5:1]
D[31:0]
XC
XV
XIN[1]
XIN[0]
VALIN[5:1]
LINKIN[31:0]
RQSTLINK2OLINK
GRNTLINK2OLINK
RQSTLINK2FIFO
GRNTLINK2FIFO
N[4..1]
LOOKUPTABLE[3..0]
LOOKUPTABLE[4]
& OR
C*V
C+V
SERCOUNT[]=255
RQSTLINK2DSPSER
GRNTLINK2DSPSER
XC*XV
Q+1
ZERO
LINK2DSPSERDATA[8] ← XV*$\overline{XC}$
LINK2DSPSERDATA[7:0]