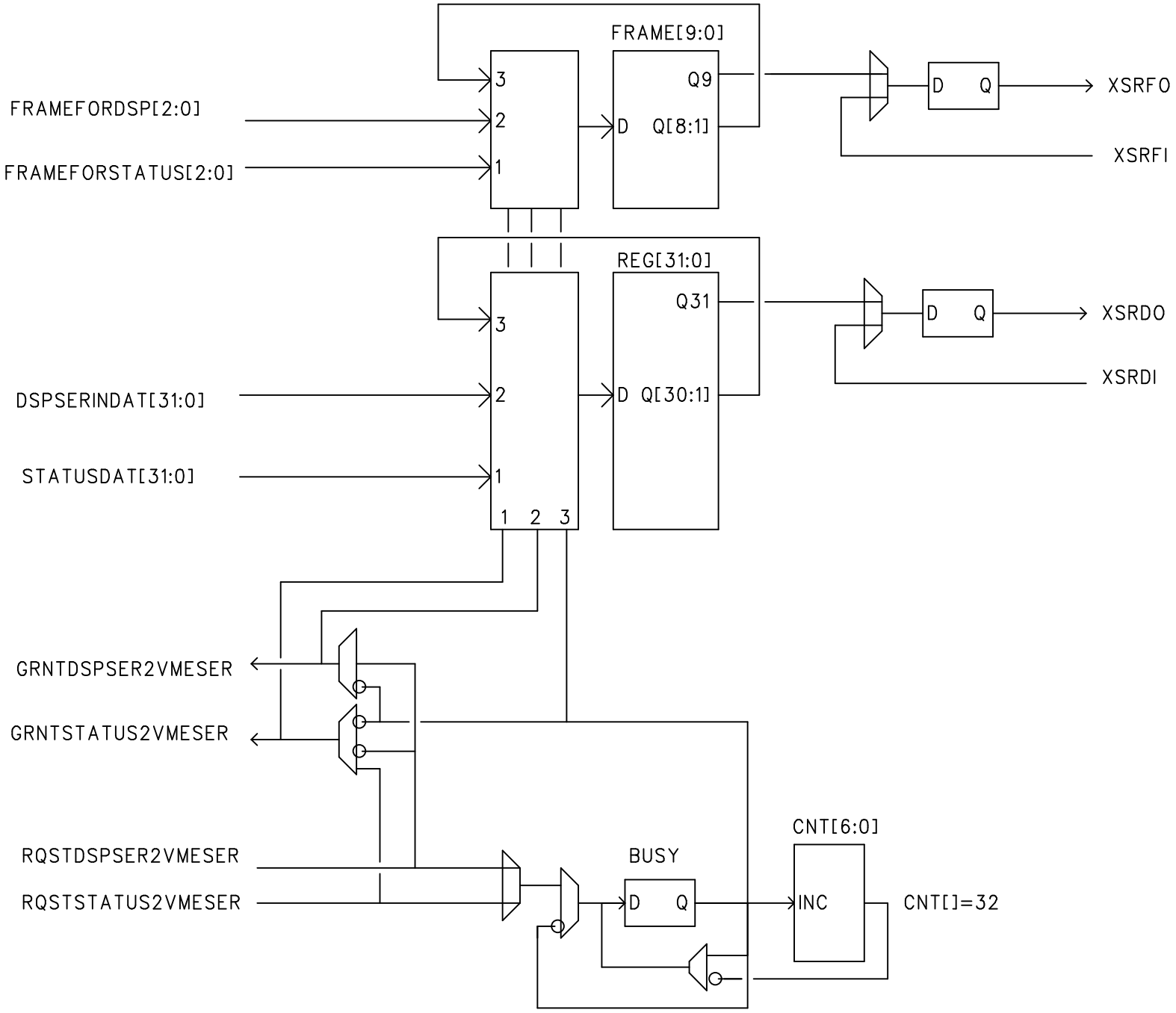DSPFPGA/VMESERR