

SERIAL BUS COMMANDS FOR TAB chips 10

Signals used:

to the TAB:

serfrm

seradr

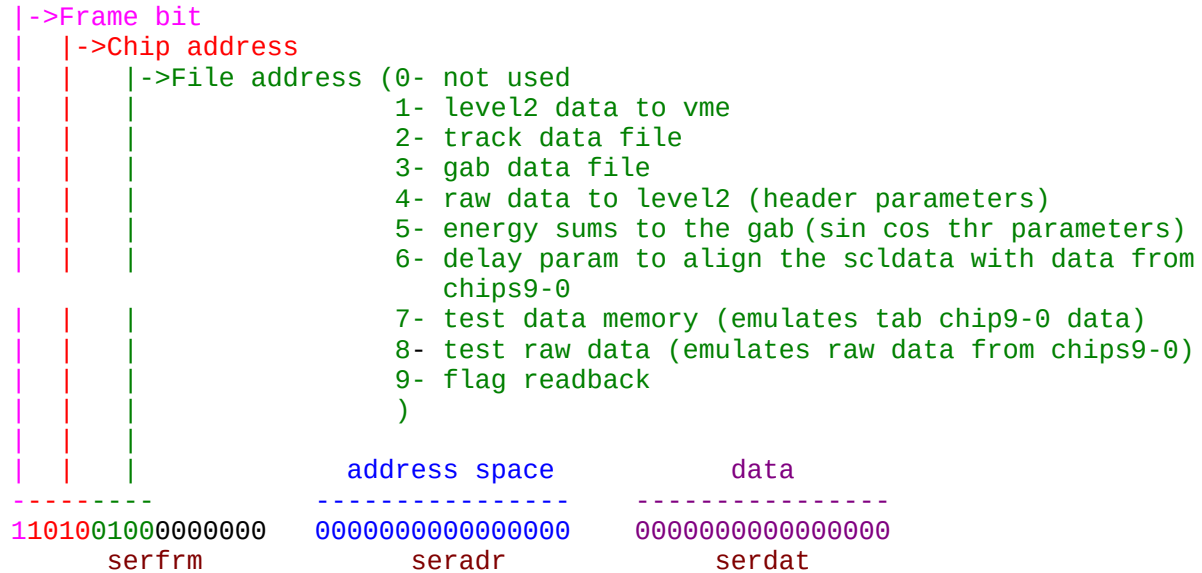
serdata

from the TAB

serfrmout

ser_dat_out

Protocol format:



COMMANDS:

--scl delay for chip#10

1101001000000000 0000000000000111 0000000000000000

-- L2/L3 header

Type	19	18	17	16	15	14	13	12	11	10	09	08	07	06	05	04	03	02	01	00
H	0	1	0	1	Header Length								No. of Objects							
H	0	0	0	0	H Format	Obj Format							Object Length							
H	0	0	0	0	Crossing No. (L1_BX)								L2 Data Type							
H	0	0	0	0	Turn No. (L1_TURN)															
H	0	0	0	0	Firmware Min Ver								Firmware Maj Ver							
H	0	0	0	0	L2 Status								Processor Bits							

--No. of Objects

1101001000000000 0000000000000000 0000000000000000

-- Header Length

1101001000000000 0000000000000000 0000000000000000

-- Object Length

1101001000000000 0000000000000001 0000000000000000

-- Format

1101001000000000 0000000000000001 0000000000000000

-- L2 Data Type

1101001000000000 0000000000000010 0000000000000000

-- Firmware Maj Ver

1101001000000000 0000000000000100 0000000000000000

-- Firmware Min Ver

1101001000000000 0000000000000100 0000000000000000

-- Processor Bits

1101001000000000 0000000000000101 0000000000000000
