SERIAL BUS COMMANDS FOR TAB chips 10

Signals used:

to the TAB:
  serfrm              seradr             serdata

from the TAB
  serfrmout                             ser_dat_out

Protocol format:

|->Frame bit
|  |->Chip address
|   |->File address (0- not used
|   |   1- level2 data to vme
|   |   2- track data file
|   |   3- gab data file
|   |   4- raw data to level2 (header parameters)
|   |   5- energy sums to the gab (sin cos thr parameters)
|   |   6- delay param to align the scdata with data from chips9-0
|   |   7- test data memory (emulates tab chip9-0 data)
|   |   8- test raw data (emulates raw data from chips9-0)
|   |   9- flag readback
|   )

|   | address space          data
|   ------------          --------------
|  1101001000000000   0000000000000000   0000000000000000
  serfrm         seradr         serdat
**COMMANDS:**

--scl delay for chip#10
```
1101001000000000   0000000000000111    0000000000000000
```

-- L2/L3 header

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-- No. of Objects
```
1101001000000000   0000000000000000    0000000000000000
```

-- Header Length
```
1101001000000000   0000000000000000    0000000000000000
```

-- Object Length
```
1101001000000000   0000000000000001    0000000000000000
```

-- Format
```
1101001000000000   0000000000000001    0000000000000000
```

-- L2 Data Type
```
1101001000000000   0000000000000100    0000000000000000
```

-- Firmware Maj Ver
```
1101001000000000   0000000000000000    0000000000000000
```

-- Firmware Min Ver
```
1101001000000000   0000000000000000    0000000000000000
```

-- Processor Bits
```
1101001000000000   0000000000000101    0000000000000000
```