TAB/GAB Test Card

H. Evans Columbia/Nevis

• Building Card to Test TAB & GAB Inputs at Nevis
  – TAB: LVDS (channel link) 424 MHz
  – GAB: LVDS (stratix) 636 MHz

• Advantages
  – Test ADF-type input before Fermilab integration
  – Verify that TAB–GAB link really works
    ▪ note: Stratix LVDS rated to >800 MHz
  – Can serve as a GAB pre-prototype
    ▪ input section firmware can be reused
  – Can be used to test production boards at Nevis

• Cost & Schedule
  – Estimated Cost ~$2000
  – Layout ~Finished
  – Firmware ~1 week
  – Board in Hand ~12-Sep-03
TAB/GAB Test Card Layout

Recycle Firmware
- Cyclone: from TAB
- Stratix rcvr: use in GAB

Loopback (TAB-to-GAB rate)

TAB (ADF sim)

TAB (TAB-to-GAB sim)