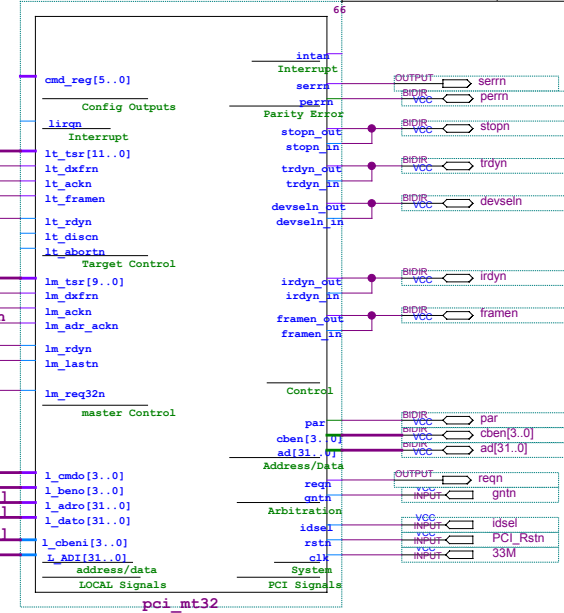
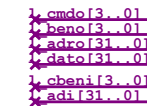
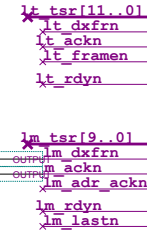
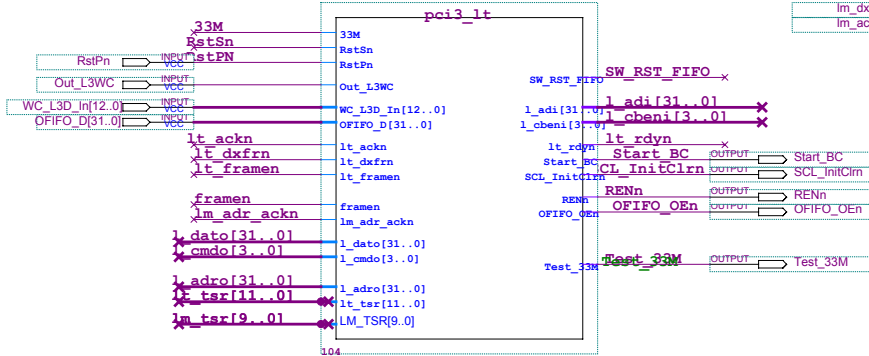
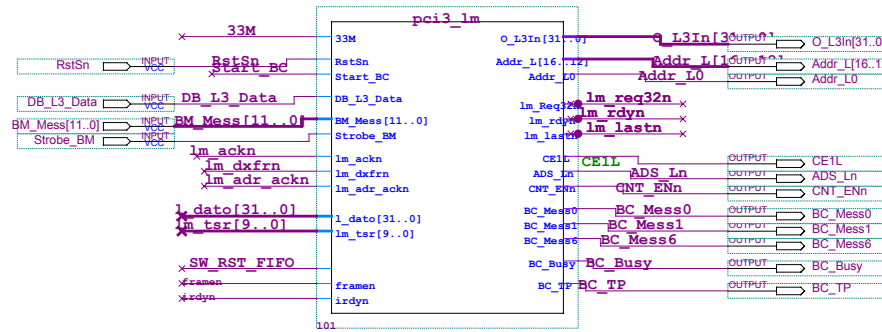


Date: June 12, 2003

bc_pci3.gdf*

Project:



Parameter	Value
BAR0	"H'FFF00008"
BAR1	"H'FFF00000"
BAR2	"H'FFF00000"
BAR3	"H'FFF00000"
BAR4	"H'FFF00000"
BAR5	"H'FFF00000"
CAP_PTR	"H'40"
CIS_PTR	"H'00000000"
CLASS_CODE	"H'FF0000"
DEVICE_ID	"H'0004"
ENABLE_BITS	"H'8000"
EXP_ROM_BAR	"H'FFF00000"
HARDWARE_BAR0	"H'47b00000"
HARDWARE_BAR1	"H'00000000"
HARDWARE_BAR2	"H'00000000"
HARDWARE_BAR3	"H'00000000"
HARDWARE_BAR4	"H'00000000"
HARDWARE_BAR5	"H'00000000"
HARDWARE_EXP_ROM	"H'00000000"
INTERNAL_ARBITER	"NO"
INTERRUPT_PIN_REG	"H'0"
MAX_LATENCY	33
MIN_GRANT	8
NUMBER_OF_BARS	1
PCI_66MHZ_CAPABLE	"no"
REVISION_ID	"H'01"
SUBSYSTEM_ID	"H'0000"
SUBSYSTEM_VENDOR_ID	"H'0000"
TARGET_DEVICE	"EPF10K50EQC240"
VENDOR_ID	"H'2001"