Buffer Controller: Reading the L3 Output FIFO

code version: 20-Dec-02
created in: pci3_lt.tdf

lt_framen

Single_R
SR_L3Data
lt_dxfrn
lt_ackn
BR_L3Data
lt_framen

l_cmdo[3..0] = 0x6
lt_tsr[11..0] = 0x101 # 0x501
l_adro[19..14] = b"1001 11"

Burst_R
BR_L3Data
lt_ackn
lt_dxfrn

l_cmdo[3..0] = 0x6 # 0xC
lt_tsr[11..0] = 0x301 # 0x701
l_adro[19..14] = b"1001 01"

WC_Equal
compare = WC_O (from WC FIFO)

WC_L3D_O

cnt_en
WC_Output
sCLR

Q
D
END_Ren
CLR
Q
D
END_Latch
CLR

33M
RstSn
33M
RstSn
33M
RstSn

33M
CLR
33M
CLR

OREN
OFIFO_OEn
BC: Holding off UII Spurious Burst Read Requests

code version: 13-Jan-03
created in: pci3_lt.tdf