Buffer Controller: Signals to BM

code version: 20-May-03
created in: pci3_lm.tdf, b_ctrl.tdf

Signal Transition Significance

- \( \text{xxx\_DONE} = \text{xxx\_DONE}[1] \& \text{xxx\_DONE}[2] \& \ldots \) (active hi)
- \( \text{xxx\_DONEn} = \text{xxx\_DONEn}[1] \# \text{xxx\_DONEn}[2] \# \ldots \) (active lo)
- \( \text{xxx\_DONE} 0 \rightarrow 1 \) all BC’s done with PUT/GET
- \( \text{xxx\_DONEn} 1 \rightarrow 0 \) 1st BC done with PUT/GET
- \( \text{xxx\_DONEn} 0 \rightarrow 1 \) all BC’s received PUT/GET\_END